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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Krishnamurthy Vaidyanathan and Geoffrey Burns

Serial No. : Unallocated

Filed : Unallocated

Title : PROGRAMMABLE DELAY INDEXED DATA

PATH REGISTER FILE FOR ARRAY PROCESSING

**Examiner** : Unallocated

Group Art Unit : Unallocated

Attorney Docket No. : US010642

## INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents Washington, D.C. 20231

Sir:

Applicant respectfully submits this Information Disclosure Statement and a copy of the cited references pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98 in order to comply with the duty of disclosure. The enclosed Form PTO-1449 identifies the references of which Applicant is aware.

The Examiner is respectfully requested to consider the references herein and make them of record in the subject application. It is Applicant's position that the references cited pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98 are clearly not a bar to allowance of the claims in this application.



The above-captioned application has not yet received a substantive Office Action, and accordingly it is believed that no fee is necessary. However, should the Patent Office determine that a fee is due, the Commissioner is authorized to charge any such fee to Deposit Account No. 11-0223.

Respectfully submitted,

KAPLAN & GILMAN, L.L.P. Attorneys for Applicants 900 Route 9 South Woodbridge, New Jersey 07095

Telephone: (732) 634-7634

Dated: December 21, 2001

By:

Aaron S. Haleva Reg. No. 44,733 FORM PTO-1449 . U.S. DEPARTMENT OF COMMERCE (Rev. 2-32) PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)

ATTORNEY DOCKET NO.: US010642

SERIAL NO.: (Not yet assigned)

APPLICANT: Krishnamurthy Vaidyanathan and Geoffrey Burns

FILING DATE: (Not yet assigned)

GROUP: (Not yet assigned)

## **U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5,644,677	7/1/97	Sangil Park, et al.	395	2.16	
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## OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

	ВВ	Proakis and Manolakis - Introduction to Digital Signal Processing - MacMillan Publishing Company NY - 1988 - ISBN: 0-02-396810-9, Section 8.5.4 pp: 662-670, specific figures of relevance Figure 8.89 and 8.90 pages 667 and 668 respectively.			
	СС	RD16023 - Technical Reference & Programmers Manual, Authors: H. Bauer, D. Lorenz (TCMC), K. Moerman, P. Kievits, H. Dijkstra, E. Rotte, J. Katenbrink, Document I.D. ASG/ESTC-99.0003 V1.3, Modification date: 20 May 1999, Document State Version 1.3 PROPOSAL, Philips Electronics N.V., 1996-1999, Pages 4-7, 56-67.			
DATE CONSIDERED					